

REMARKS

Claims 1-18 stand rejected under 35 U.S.C §102(a) as being anticipated by U.S. Pat. No. 6,323,871 to Fujiyoshi et al. ("Fujiyoshi"). Applicant respectfully traverses the rejection. However, to expedite prosecution in the present application, claims 1, 9, and 10 have been amended and claims 19-21 have been added to better define the invention. Claims 4-9 and 12-18 have also been amended to correct typographical errors.

Fujiyoshi is directed to a matrix-addressed display device and its driving method. Fujiyoshi discloses reducing the number of source drivers in a liquid crystal display ("LCD") by changing the arrangement of the pixel electrodes in a picture element and accordingly modifying the data stream input to the source drivers. To do this, Fujiyoshi discloses a plurality of source lines and gate lines arranged in a matrix on a substrate, with each of the source lines extending across each of the gate lines in all embodiments. However, Fujiyoshi does not disclose or suggest dividing a plurality of source lines into two groups in a direction of extension of the source lines, such that at least one of the source lines does not extend across at least one of the gate lines.

The Office Action cites Col. 1, lines 39-56, Fig. 8, and Fig. 17 of Fujiyoshi, which is labeled "PRIOR ART," in support of the rejection of claims 1 and 10. Particularly, for the claim elements involving dividing the plurality of source and gate lines, Fig. 17 is referenced as anticipating those elements. Fig. 17 discloses an LCD with eight source drivers Sd1, Sd2, Sd3, Sd4, Sd5, Sd6, Sd7, Sd8 and two gate drivers Gd1, Gd2. The source drivers Sd1, Sd5 are shown as applying signals to source lines S1, S3 and S2, S4, respectively. Each source line extends across each of the gate lines in this figure—e.g., S1 extends across G1, G2, and the lines to which signals are applied by gate driver Gd2. This arrangement resembles "PRIOR ART" Fig. 3 of the current application, wherein, for example, the source drivers 105, 106 are shown as applying signals to source lines S1, S2 and S3, S4, respectively. Each source line also extends across each of the gate lines in this particular figure—e.g., S1 extends across G1, G2, G3, ...,

Gn.

Conversely, amended claims 1 and 10 and new claim 19 recite "that at least one of the source lines does not extend across at least one of the gate lines." Neither the "PRIOR ART" Fig. 17 of Fujiyoshi nor the "PRIOR ART" Fig. 3 of the current application disclose or suggest at least one of the source lines not extending across at least one of the gate lines. Therefore, for at least these reasons, Applicant respectfully requests that the rejection to claims 1 and 10 be withdrawn and new claim 19 be allowed.

Furthermore, claims 2-9 and 21, 11-18, and 20 depend, directly or indirectly, from claims 1, 10, and 19, respectively. These claims include all of the features of their respective independent claims, plus additional features. Therefore, for at least the reasons discussed above with respect to claims 1, 10, and 19, Applicant respectfully requests that the rejection of claims 2-9 and 11-18 be withdrawn and that claims 20 and 21 be allowed.

For all of the above reasons, Applicant respectfully requests reconsideration and allowance of the present application. The Examiner is invited to contact the undersigned attorney at (312) 321-4200 if there are any outstanding issues that could be resolved through a telephone conference.

Respectfully submitted,



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